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(71)Applicant : FUJITSU GENERAL LTD

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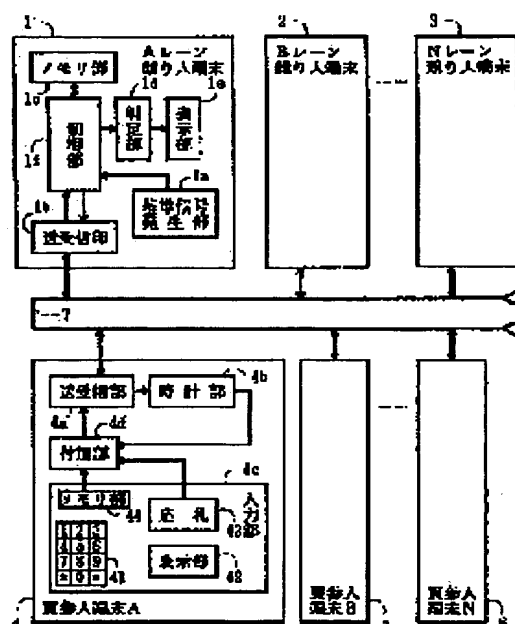
(72)Inventor : UDA TAKASHI

(54) AUCTION SYSTEM

(57)Abstract:

PROBLEM TO BE SOLVED: To improve the judging precision of the order of auction bidding entries from plural bidding entry terminals.

SOLUTION: At the time of the start of auction, an auction start signal is generated as a reference signal from a reference signal generation part 1a of a bidder terminal 1 or the like and is transmitted to each bidding entry terminal 4 through a transmission/reception part 1b and a bus line 7. When this reference signal is received by a transmission/reception part 4a, a clock part 4b of the bidding entry terminal 4 or the like outputs time data and adds this time data to bidding entry data from an input part 4c (by a time data addition part 4d). Bidding entry data to which this time data is added is transmitted to the bidder terminal 1 or the like through the bus line 7. The bidder terminal side stores bidding entry data, where time data is added, from each bidding entry terminal in a memory part 1c and discriminates the transmission order of bidding entry terminals based on signals indicated by time data added to bidding entry data by a judging part 1d or the like. the judged result is displayed on a display part 1e.



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[Patent number]

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08.11.2002

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CLAIMS

[Claim(s)]

[Claim 1] In the auction market system to which it comes to connect two or more 3 ** terminal and two or more auction market people terminals by the bus line The reference signal generating section which generates the auction market start signal which shows initiation of an auction market as a reference signal, and transmits to said 3 ** terminal through said bus line, The memory section which memorizes each tender data with which the tender data which were emitted from said each 3 ** terminal through said bus line, and with which time signal data were added were received, and coincidence news data were added, The judgment section which judges the dispatch ranking of a 3 ** terminal from the time signal data added to the tender data memorized by said memory section, The clock section which outputs time signal data when said auction market start signal is received through said bus line while preparing the control section which controls said reference signal generating section, the memory section, and the judgment section in said each of auction market people terminal, The auction market system characterized by preparing the time signal data adjunct added to the tender data into which the time signal data from said clock section were inputted from the input section in said each 3 ** terminal, and judging the dispatch ranking of two or more 3 ** terminals of each based on said time signal data.

[Claim 2] In the auction market system to which it comes to connect two or more 3 ** terminal and two or more auction market people terminals by the bus line The reference signal generating section which generates the auction market start signal which shows initiation of an auction market as a reference signal, and transmits to said 3 ** terminal through said bus line, The memory section which memorizes each tender data with which the tender data which were emitted from said each 3 ** terminal through said bus line, and with which count data were added were received, and these count data were added, The judgment section which judges the dispatch ranking of a 3 ** terminal from the count data added to the tender data memorized by said memory section, The clock signal generating section which generates a clock signal while preparing the control section which controls said reference signal generating section, the memory section, and the judgment section in said each of auction market people terminal, The counter which counts the clock signal from said clock signal generating section when said auction market start signal is received through said bus line, and outputs count data, The auction market system characterized by preparing the count data adjunct added to the tender data into which the count data from said counter were inputted from the input section in said each 3 ** terminal, and judging the dispatch ranking of two or more 3 ** terminals of each based on said count data.

[Claim 3] the auction market system according to claim 1 or 2 characterized by acquiring and making dispatch ranking from the 3 ** terminal which prepared the display in said each of auction market people terminal, and was judged by said judgment section into ** displayed on said display.

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DETAILED DESCRIPTION

[Detailed Description of the Invention]

[0001]

[Industrial Application] This invention relates to an auction market system, and relates to improvement in the judgment precision of the auction market tender ranking from each 3 ** terminal which it comes to prepare in a detail more. [two or more]

[0002]

[Description of the Prior Art] Drawing 3 is an important section block diagram for explaining the conventional auction market system. As shown in this drawing, the auction market system constitutes based on a necessary method by connection (27) Carrying out two or more auction market people terminals 21, 22, and 23 and two or more 3 ** terminals 24, 25, and 26. One of the connection types of this auction market people terminal and a 3 ** terminal has LAN connection of CSMA/CD. [CSMA/CD =Carrier Sense Multiple Access With Collision Detection]

[0003] Three ** carry out input assignment of the lane treating the goods with which self wishes to purchase from the input section of 3 ** terminal 24 grade, and enter into the auction market of the lane. It is displayed with the display of each auction market people terminal 21 grade about the goods with which an auction market is presented, its price, quantity, etc. When [at which the auction market people side showed] it competes and 3 ** tender a bid to a price, the purport which tenders a bid from the input section of 3 ** terminal 24 grade is inputted. The data concerning this tender are transmitted to an auction market people side through the bus line 27 based on the above-mentioned method. Therefore, when there are two or more tender candidates, tender data will be transmitted to an auction market people side from two or more 3 ** terminals, respectively. The auction market people side who received the tender data from two or more 3 ** terminals had determined 3 ** based on the receiving ranking of tender data.

[0004]

[Problem(s) to be Solved by the Invention] However, when two or more 3 ** tender a bid within coincidence or the near time amount in the LAN connection by said CSMA/CD, there is a problem that the tender data does not compete in order of the timing which carried out the tender input, and does not necessarily reach to a man side. On the other hand, conventionally, the receiving ranking by the side of auction market people had determined 3 ** as mentioned above. Although this carried out the tender input first, it will mean that the fault that the receiving ranking by the side of auction market people becomes behind has arisen, and will lack in fairness. Thus, it is from the following reasons that the receiving ranking by the side of auction market people does not agree with the entry sequence of a 3 ** terminal.

[0005] Since the transmission route is common, if a data input is carried out from two or more 3 ** terminals within coincidence or the time amount which approached extremely in bus-line connection, a collision will arise to data. When this collision arises, transmission of one of terminals is suspended automatically and processing in which it retransmits a message after fixed time amount is taken by the 3 ** terminal side. In this case, of which terminal transmission is suspended changes with situations of a collision of data. Since it is such, it arises that the receiving ranking by the side of auction market people does not agree with the entry sequence of a 3 ** terminal. Solution of the above troubles is benefited for this invention, and it aims at the thing which planned fairness of an auction market and for which it competes and a system is offered.

[0006]

[Means for Solving the Problem] In the auction market system to which, as for this invention, it comes to connect two or more 3 ** terminal and two or more auction market people terminals by the bus line The

reference signal generating section which generates the auction market start signal which shows initiation of an auction market as a reference signal, and transmits to said 3 ** terminal through said bus line, The memory section which memorizes each tender data with which the tender data which were emitted from said each 3 ** terminal through said bus line, and with which time signal data were added were received, and coincidence news data were added, The judgment section which judges the dispatch ranking of a 3 ** terminal from the time signal data added to the tender data memorized by said memory section, The clock section which outputs time signal data when said auction market start signal is received through said bus line while preparing the control section which controls said reference signal generating section, the memory section, and the judgment section in said each of auction market people terminal, It is the thing which prepares the time signal data adjunct added to the tender data into which the time signal data from said clock section were inputted from the input section in said each 3 ** terminal, and judged the dispatch ranking of two or more 3 ** terminals of each based on said time signal data and which competes and offers a system.

[0007]

[Function] When starting an auction market, from the reference signal generating section of an auction market people terminal, the auction market start signal which shows initiation of an auction market is sent, and this is transmitted to each 3 ** terminal through a bus line. The clock section by the side of each 3 ** terminal starts the output of time signal data, when said auction market start signal is received, and it adds this time signal data to tender data in the case of a tender. This time signal data is added by the time signal adjunct. The tender data with which time signal data were added are sent to an auction market people terminal. An auction market people side receives the tender data with which said time signal data were added, and once memorizes them in the memory section. The judgment section judges the dispatch ranking of a 3 ** terminal based on the time signal data added to the tender data memorized by the memory section.

[0008]

[Example] Hereafter, based on a drawing, the auction market system by this invention is explained. The important section block diagram and drawing 2 which show one example of the auction market system according [drawing 1] to this invention are the important section block diagram showing other examples. In drawing 1 , A rain auction market people terminal 1, 2, and whose 3 are the sponsor sides of an auction market, B rain auction market people terminal and N rain auction market people terminal, the 3 ** terminal A with which 3 ** which enter into an auction market operate 4, 5, and 6, the 3 ** terminal B and the 3 ** terminal N, and 7 are bus lines which connect the auction market people terminal which consists of the above-mentioned plurality, and a 3 ** terminal. The reference signal generating section which generates the auction market start signal with which 1a shows initiation of an auction market as a reference signal in A rain auction market people terminal 1, The transceiver section in which 1b makes signal transfer with 3 ** terminals with each through a bus line 7, The memory section which memorizes the tender data from the 3 ** terminals from each which 1c received by transceiver section 1b, The judgment section which judges the dispatch ranking of a 3 ** terminal based on the time signal data added to the tender data memorized by 1d memory section 1c, They are the display which displays the dispatch ranking which judged 1e in 1d of judgment sections, and the control section which controls 1f of reference signal generating section 1a, transceiver section 1b, memory section 1c, and 1d of judgment sections. B rain auction market people terminal 2 and N rain auction market people terminal 3 -- since each configuration was the same as A rain auction market people terminal 1, illustration was omitted (drawing 2 and drawing 3 are also **).

[0009] The transceiver section in which 4a makes signal transfer with one of auction market people terminals through a bus line 7 in 3 ** terminal A4, The clock section which starts the output of time signal data when 4b receives said auction market start signal by transceiver section 4a, The input section which inputs necessary data, such as delimiters (ID etc.) for 4c to specify assignment of the lane which enters into an auction market, and 3 **, or a tender, and 4d are time signal data adjuncts which add the time signal data from clock section 4b to the tender data from input section 4b. 3 ** terminal B5 and the 3 ** terminal N6 -- since each configuration was the same as 3 ** terminal A4, illustration was omitted (drawing 2 and drawing 3 are also **). In drawing 2 , it is the counter which the thing equivalent to drawing 1 has attached the same sign, the clock signal generating section and 11c in which 11b in the 3 ** terminal A11 generates a clock signal count the clock signal from clock signal generating section 11b when said auction market start signal is received by transceiver section 11a, and outputs count data. About a block of others of a 3 ** terminal, it is the thing of drawing 1 and this function.

[0010] Next, it divides and explains for every Fig. per actuation of this invention.

(1) the explanation auction market of the example of drawing 1 -- as a principle -- each of each lane -- go on

separately. Then, the auction market people terminals 1, 2, and 3 of each lane transmit an auction market start signal to each 3 ** terminal side to compensate for initiation of an auction market. In this case, the signal which pinpoints a lane is included in an auction market start signal. It is made to make it generate from reference signal generating section 1a by making this auction market start signal into a reference signal, and this generating section is prepared for every lane. This auction market start signal is transmitted to each 3 ** terminals 4, 5, and 6 through transceiver section 1b and a bus line 7. The above-mentioned bus line 7 consists of LANs of the CSMA/CD of the above-mentioned (Prior art).

[0011] Clock section 4b is prepared in the each 3 ** terminal 4 grade, and when transceiver section 4a of this terminal receives the above-mentioned auction market start signal, time signal data are outputted from this clock section 4b. Although the above-mentioned time signal data mean current time of day, you may make it include the time of day below a second unit in consideration of the property of an auction market. This time signal data is added to the tender data from input section 4c in 4d of time signal data adjuncts. In addition, in input section 4c, the memory section 44 grade which memorizes the ten key 41 which inputs a delimiter (ID), purchase quantity, etc. for specifying 3 **, the display 42 which displays the inputted contents, the tender key 43 operated in the case of a tender, Above ID, purchase quantity, etc. is prepared, for example like illustration. The above-mentioned tender data mean what included the data when operating the tender key 43 in the data memorized by the memory section 44.

[0012] This added signal is transmitted to the auction market people terminal (here, it considers as A rain auction market people terminal 1) of the lane which has entered through transceiver section 4a and a bus line 7. At the auction market people terminal 1, each tender data from each 3 ** terminal received by transceiver section 1b is memorized to memory section 1c. In this case, the tender data received in time amount a break and in the meantime are memorized. 1d of judgment sections judges 3 ** terminal side dispatch ranking on the basis of the early order of the time signal data added to tender data by the bottom of control of 1f of control sections, and the 3 ** terminal side. Moreover, this judgment result is displayed on display 1e. The above is actuation of drawing 1 .

[0013] (2) Replace the explanatory view 2 of the example of drawing 2 with time signal data addition of drawing 1 , and it adds count data to a 3 ** terminal side.

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TECHNICAL FIELD

[Industrial Application] This invention relates to an auction market system, and relates to improvement in the judgment precision of the auction market tender ranking from each 3 ** terminal which it comes to prepare in a detail more. [two or more]

[0002]

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PRIOR ART

[Description of the Prior Art] Drawing 3 is an important section block diagram for explaining the conventional auction market system. As shown in this drawing, the auction market system constitutes based on a necessary method by connection (27) Carrying out two or more auction market people terminals 21, 22, and 23 and two or more 3 ** terminals 24, 25, and 26. One of the connection types of this auction market people terminal and a 3 ** terminal has LAN connection of CSMA/CD. [CSMA/CD =Carrier Sense Multiple Access With Collision Detection]

[0003] Three ** carry out input assignment of the lane treating the goods with which self wishes to purchase from the input section of 3 ** terminal 24 grade, and enter into the auction market of the lane. It is displayed with the display of each auction market people terminal 21 grade about the goods with which an auction market is presented, its price, quantity, etc. When [at which the auction market people side showed] it competes and 3 ** tender a bid to a price, the purport which tenders a bid from the input section of 3 ** terminal 24 grade is inputted. The data concerning this tender are transmitted to an auction market people side through the bus line 27 based on the above-mentioned method. Therefore, when there are two or more tender candidates, tender data will be transmitted to an auction market people side from two or more 3 ** terminals, respectively. The auction market people side who received the tender data from two or more 3 ** terminals had determined 3 ** based on the receiving ranking of tender data.

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EFFECT OF THE INVENTION

[Effect of the Invention] As explained above, in this invention, either of the time signals which it competed for and were emitted from the auction market people terminal side in the system, for which it competed and which were generated in the 3 ** terminal side based on the start signal or count data which were built by LAN of CSMA/CD etc. was added to the tender data by the side of a 3 ** terminal. Therefore, in an auction market people side, the dispatch ranking by the side of a just 3 ** terminal can be judged now based on coincidence news or count data. By this, conventionally, even if there is fault that the dispatch ranking by the side of 3 ** resulting from a collision of the data on a bus line produced in the system configuration by the above-mentioned LAN connection and the receiving ranking by the side of auction market people are not necessarily in agreement, just dispatch ranking can be judged. As mentioned above, the fairness of an auction market is maintained by this invention.

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TECHNICAL PROBLEM

[Problem(s) to be Solved by the Invention] However, when two or more 3 ** tender a bid within coincidence or the near time amount in the LAN connection by said CSMA/CD, there is a problem that the tender data does not compete in order of the timing which carried out the tender input, and does not necessarily reach to a man side. On the other hand, conventionally, the receiving ranking by the side of auction market people had determined 3 ** as mentioned above. Although this carried out the tender input first, it will mean that the fault that the receiving ranking by the side of auction market people becomes behind has arisen, and will lack in fairness. Thus, it is from the following reasons that the receiving ranking by the side of auction market people does not agree with the entry sequence of a 3 ** terminal.

[0005] Since the transmission route is common, if a data input is carried out from two or more 3 ** terminals within coincidence or the time amount which approached extremely in bus-line connection, a collision will arise to data. When this collision arises, transmission of one of terminals is suspended automatically and processing in which it retransmits a message after fixed time amount is taken by the 3 ** terminal side. In this case, of which terminal transmission is suspended changes with situations of a collision of data. Since it is such, it arises that the receiving ranking by the side of auction market people does not agree with the entry sequence of a 3 ** terminal. Solution of the above troubles is benefited for this invention, and it aims at the thing which planned fairness of an auction market and for which it competes and a system is offered.

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MEANS

[Means for Solving the Problem] In the auction market system to which, as for this invention, it comes to connect two or more 3 ** terminal and two or more auction market people terminals by the bus line The reference signal generating section which generates the auction market start signal which shows initiation of an auction market as a reference signal, and transmits to said 3 ** terminal through said bus line, The memory section which memorizes each tender data with which the tender data which were emitted from said each 3 ** terminal through said bus line, and with which time signal data were added were received, and coincidence news data were added, The judgment section which judges the dispatch ranking of a 3 ** terminal from the time signal data added to the tender data memorized by said memory section, The clock section which outputs time signal data when said auction market start signal is received through said bus line while preparing the control section which controls said reference signal generating section, the memory section, and the judgment section in said each of auction market people terminal, It is the thing which prepares the time signal data adjunct added to the tender data into which the time signal data from said clock section were inputted from the input section in said each 3 ** terminal, and judged the dispatch ranking of two or more 3 ** terminals of each based on said time signal data and which competes and offers a system.

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OPERATION

[Function] When starting an auction market, from the reference signal generating section of an auction market people terminal, the auction market start signal which shows initiation of an auction market is sent, and this is transmitted to each 3 ** terminal through a bus line. The clock section by the side of each 3 ** terminal starts the output of time signal data, when said auction market start signal is received, and it adds this time signal data to tender data in the case of a tender. This time signal data is added by the time signal adjunct. The tender data with which time signal data were added are sent to an auction market people terminal. An auction market people side receives the tender data with which said time signal data were added, and once memorizes them in the memory section. The judgment section judges the dispatch ranking of a 3 ** terminal based on the time signal data added to the tender data memorized by the memory section.

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EXAMPLE

[Example] Hereafter, based on a drawing, the auction market system by this invention is explained. The important section block diagram and drawing 2 which show one example of the auction market system according [drawing 1] to this invention are the important section block diagram showing other examples. In drawing 1 , A rain auction market people terminal 1, 2, and whose 3 are the sponsor sides of an auction market, B rain auction market people terminal and N rain auction market people terminal, the 3 ** terminal A with which 3 ** which enter into an auction market operate 4, 5, and 6, the 3 ** terminal B and the 3 ** terminal N, and 7 are bus lines which connect the auction market people terminal which consists of the above-mentioned plurality, and a 3 ** terminal. The reference signal generating section which generates the auction market start signal with which 1a shows initiation of an auction market as a reference signal in A rain auction market people terminal 1, The transceiver section in which 1b makes signal transfer with 3 ** terminals with each through a bus line 7, The memory section which memorizes the tender data from the 3 ** terminals from each which 1c received by transceiver section 1b, The judgment section which judges the dispatch ranking of a 3 ** terminal based on the time signal data added to the tender data memorized by 1d memory section 1c, They are the display which displays the dispatch ranking which judged 1e in 1d of judgment sections, and the control section which controls 1f of reference signal generating section 1a, transceiver section 1b, memory section 1c, and 1d of judgment sections. B rain auction market people terminal 2 and N rain auction market people terminal 3 -- since each configuration was the same as A rain auction market people terminal 1, illustration was omitted (drawing 2 and drawing 3 are also **).

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[0010] Next, it divides and explains for every Fig. per actuation of this invention.

(1) the explanation auction market of the example of drawing 1 -- as a principle -- each of each lane -- go on separately. Then, the auction market people terminals 1, 2, and 3 of each lane transmit an auction market start signal to each 3 ** terminal side to compensate for initiation of an auction market. In this case, the signal which pinpoints a lane is included in an auction market start signal. It is made to make it generate from reference signal generating section 1a by making this auction market start signal into a reference signal, and this generating section is prepared for every lane. This auction market start signal is transmitted to each 3 ** terminals 4, 5, and 6 through transceiver section 1b and a bus line 7.

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DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the important section block diagram showing one example of the auction market system by this invention.

[Drawing 2] It is the important section block diagram showing other examples of the auction market system by this invention.

[Drawing 3] It is an auction market system important section block diagram for explaining the conventional auction market system.

[Description of Notations]

1 A Rain Auction Market People Terminal

1a Reference signal generating section

1b Transceiver section

1c Memory section

1d Judgment section

1e Display

1f Control section

2 B Rain Auction Market People Terminal

3 N Rain Auction Market People Terminal

4 Three ** Terminal A

4a Transceiver section

4b Clock section

4c Input section

4d Time signal data adjunct

5 Three ** Terminal B

6 Three ** Terminal N

7 Bus Line

11b Clock signal generating section

11c Counter

11e Count data adjunct

[Translation done.]

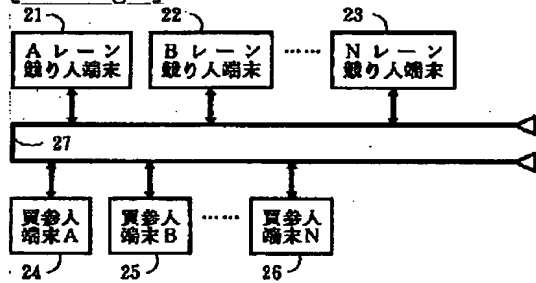
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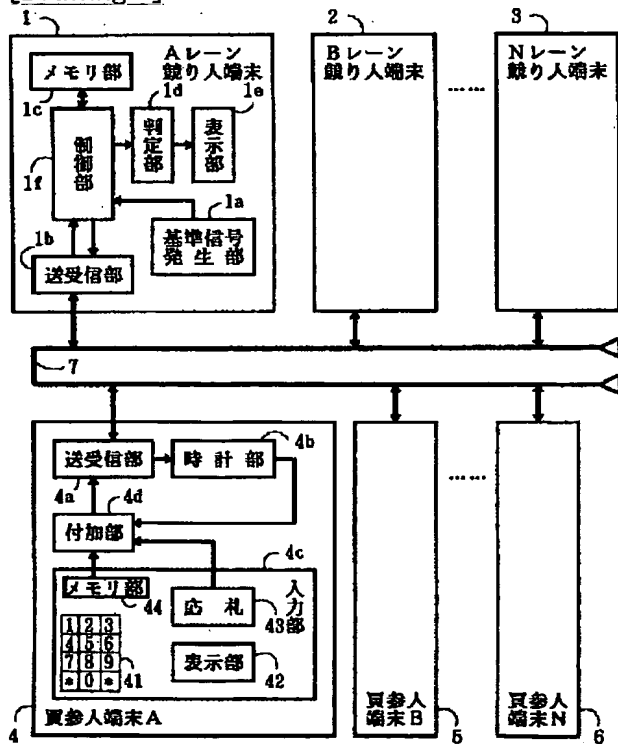
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DRAWINGS

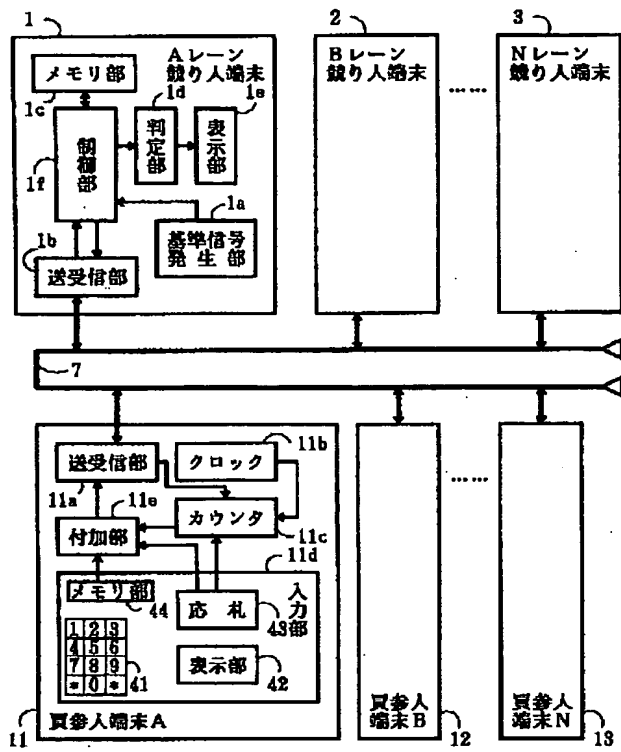
[Drawing 3]



[Drawing 1]



[Drawing 2]



[Translation done.]

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(71) 出願人 000006611

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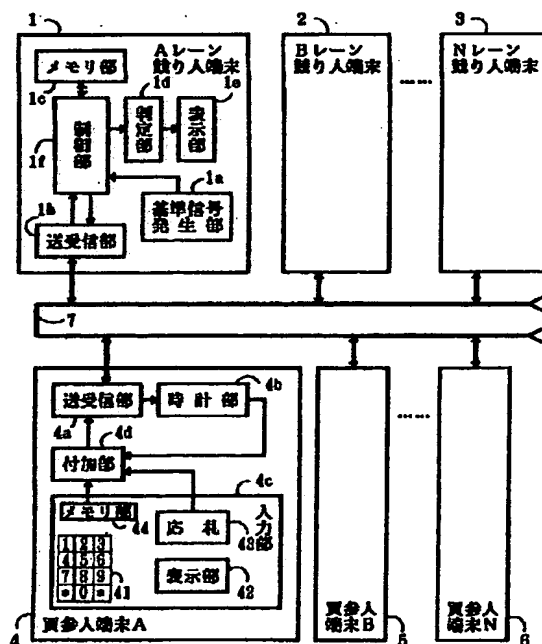
川崎市高津区末長1116番地 株式会社富士通ゼネラル内

(54) 【発明の名称】 競りシステム

(57) 【要約】

【課題】 競りシステムに関し、複数設けられてなる買参人端末それぞれよりの競り応札順位の判定精度を向上する。

【解決手段】 競りを開始する場合、競り人端末1等の基準信号発生部1aより競り開始信号を基準信号として発生し、これを送受信部1b、バスライン7を介して各買参人端末4等へ送信する。買参人端末4等の時計部4bは、送受信部4aで前記基準信号を受信したときには時報データを出し、同時報データを入力部4cよりの応札データに付加する(時報データ付加部4d)。この時報データを付加した応札データをバスライン7を介し競り人1等へ送信する。競り人側は、競り人端末のメモリ部1cに各買参人端末よりの時報データの付加された応札データを記憶し、判定部1d等により応札データに付加された時報データを示す信号を基に買参人端末の発信順位を判定する。また、判定結果を表示部1eにより表示する。



【特許請求の範囲】

【請求項1】 複数の買参人端末と、複数の競り人端末とがバスラインで接続されてなる競りシステムにおいて、競りの開始を示す競り開始信号を基準信号として発生し、前記バスラインを介し前記買参人端末へ送信する基準信号発生部と、前記バスラインを介し前記買参人端末それぞれより発せられた、時報データが付加された応札データを受信し、同時報データが付加された応札データそれぞれを記憶するメモリ部と、前記メモリ部に記憶された応札データに付加された時報データから買参人端末の発信順位を判定する判定部と、前記基準信号発生部、メモリ部及び判定部とを制御する制御部とを前記競り人端末それぞれに設ける一方、前記バスラインを介し前記競り開始信号を受信したときには時報データを出力する時計部と、前記時計部よりの時報データを、入力部より入力された応札データに付加する時報データ付加部とを前記買参人端末それぞれに設け、複数の買参人端末それぞれの発信順位を、前記時報データを基に判定するようにしたことを特徴とする競りシステム。

【請求項2】 複数の買参人端末と、複数の競り人端末とがバスラインで接続されてなる競りシステムにおいて、競りの開始を示す競り開始信号を基準信号として発生し、前記バスラインを介し前記買参人端末へ送信する基準信号発生部と、前記バスラインを介し前記買参人端末それぞれより発せられた、カウントデータが付加された応札データを受信し、同カウントデータが付加された応札データそれぞれを記憶するメモリ部と、前記メモリ部に記憶された応札データに付加されたカウントデータから買参人端末の発信順位を判定する判定部と、前記基準信号発生部、メモリ部及び判定部とを制御する制御部とを前記競り人端末それぞれに設ける一方、クロック信号を発生するクロック信号発生部と、前記バスラインを介し前記競り開始信号を受信したときには前記クロック信号発生部よりのクロック信号をカウントし、カウントデータを出力するカウンタと、前記カウンタよりのカウントデータを、入力部より入力された応札データに付加するカウントデータ付加部とを前記買参人端末それぞれに設け、複数の買参人端末それぞれの発信順位を、前記カウントデータを基に判定するようにしたことを特徴とする競りシステム。

【請求項3】 表示部を前記競り人端末それぞれに設け、前記判定部により判定された買参人端末よりの発信順位を前記表示部に表示するよいうしたことを特徴とする請求項1又は請求項2記載の競りシステム。

【発明の詳細な説明】

【0001】

【産業上の利用分野】 本発明は競りシステムに係り、より詳細には、複数設けられてなる買参人端末それぞれの競り応札順位の判定精度の向上に関する。

【0002】

【従来の技術】 図3は従来の競りシステムを説明するための要部ブロック図である。同図に示すように、競りシステムは複数の競り人端末21、22、23と、複数の買参人端末24、25、26とを所要の方式に基づき接続(27)し構成している。この競り人端末と買参人端末との接続方式の1つに、CSMA/CD方式のLAN接続がある。

【CSMA/CD = Carrier Sense Multiple Access With Collision Detection】

【0003】 買参人は、自己が購入を希望する商品を選ぶレーンを買参人端末24等の入力部より入力指定し、そのレーンの競りに参入する。競りに供される商品、及びその価格や数量等については各競り人端末21等の表示装置により表示される。競り人側が示した競り価格に対し、買参人が応札するときには買参人端末24等の入力部より応札する旨を入力する。この応札に係るデータは上記方式に基づくバスライン27を介し、競り人側へ送信される。従って、応札希望者が複数人である場合、複数の買参人端末から応札データがそれぞれ競り人側へ送信されることになる。複数の買参人端末からの応札データを受信した競り人側は、応札データの受信順位を基に買参人を決定していた。

【0004】

【発明が解決しようとする課題】 しかし、前記CSMA/CD方式によるLAN接続の場合、複数の買参人が同時、又は接近した時間内で応札したとき、その応札データが、応札入力したタイミングの順序で競り人側へ到達するとは限らないという問題がある。これに対し従来は、前述のように競り人側の受信順位により買参人を決定していた。このことは、最初に応札入力したにもかかわらず、競り人側での受信順位が後になるという不具合が生じていることを意味し、公平性に欠けることとなる。このように、買参人端末の入力順位と、競り人側の受信順位とが合致しないのは以下の理由からである。

【0005】 バスライン接続の場合、伝送経路が共通のため、複数の買参人端末から同時、又は極めて接近した時間内でデータ入力されると、データ同士にぶつかり合いが生じる。このぶつかり合いが生じた場合、いずれかの端末の送信は自動的に一旦停止され、一定時間後に再送信するという処理が買参人端末側でとられる。この場合、どの端末の送信が停止されるかはデータのぶつかり合いの状況により異なる。このようなことから、買参人端末の入力順位と、競り人側の受信順位とが合致しないということが生じるものである。本発明は上述のような問題点の解決のためになされたものであり、競りの公平性を図った競りシステムを提供することを目的とする。

【0006】

【課題を解決するための手段】 本発明は、複数の買参人端末と、複数の競り人端末とがバスラインで接続されてなる競りシステムにおいて、競りの開始を示す競り開始信号を基準信号として発生し、前記バスラインを介し前

記買参人端末へ送信する基準信号発生部と、前記バスラインを介し前記買参人端末それぞれより発せられた、時報データが付加された応札データを受信し、同時報データが付加された応札データそれぞれを記憶するメモリ部と、前記メモリ部に記憶された応札データに付加された時報データから買参人端末の発信順位を判定する判定部と、前記基準信号発生部、メモリ部及び判定部とを制御する制御部とを前記競り人端末それぞれに設ける一方、前記バスラインを介し前記競り開始信号を受信したときには時報データを出力する時計部と、前記時計部よりの時報データを、入力部より入力された応札データに付加する時報データ付加部とを前記買参人端末それぞれに設け、複数の買参人端末それぞれの発信順位を、前記時報データを基に判定するようにした競りシステムを提供するものである。

【0007】

【作用】競りを開始する場合、競り人端末の基準信号発生部より競りの開始を示す競り開始信号を発信し、これをバスラインを介して各買参人端末へ送信する。各買参人端末側の時計部は前記競り開始信号を受信されたときには時報データの出力を開始し、応札の際にはこの時報データを応札データに付加する。この時報データの付加を時報付加部で行う。時報データが付加された応札データは競り人端末へ送られる。競り人側は、前記時報データが付加された応札データを受信し、一旦、メモリ部に記憶する。判定部は、メモリ部に記憶された応札データに付加された時報データを基に買参人端末の発信順位を判定する。

【0008】

【実施例】以下、図面に基づいて本発明による競りシステムを説明する。図1は本発明による競りシステムの一実施例を示す要部ブロック図、図2は他の実施例を示す要部ブロック図である。図1において、1、2及び3は競りの主催者側であるAレーン競り人端末、Bレーン競り人端末及びNレーン競り人端末、4、5及び6は競りに参入する買参人が操作する買参人端末A、買参人端末B及び買参人端末N、7は上記複数からなる競り人端末と買参人端末とを接続するバスラインである。Aレーン競り人端末1において、1aは競りの開始を示す競り開始信号を基準信号として発生する基準信号発生部、1bはバスライン7を介し買参人端末各々との信号授受をなす送受信部、1cは送受信部1bで受信した買参人端末各々よりの応札データを記憶するメモリ部、1dはメモリ部1cに記憶された応札データに付加された時報データを基に買参人端末の発信順位を判定する判定部、1eは判定部1dで判定した発信順位を表示する表示部、1fは基準信号発生部1a、送受信部1b、メモリ部1c及び判定部1dとを制御する制御部である。Bレーン競り人端末2及びNレーン競り人端末3それぞれの構成はAレーン競り人端末1と同じであるので図示は省略した(図2、図3も同)。

【0009】買参人端末A4において、4aはバスライン7を介しいずれかの競り人端末との信号授受をなす送受信部、4bは送受信部4aにより前記競り開始信号を受信したときには時報データの出力を開始する時計部、4cは競りに参入するレーンの指定、買参人を特定するための識別記号(ID等)、又は応札等、所要のデータを入力する入力部、4dは時計部4bよりの時報データを入力部4bよりの応札データに付加する時報データ付加部である。買参人端末B5及び買参人端末N6それぞれの構成は買参人端末A4と同じであるので図示は省略した(図2、図3も同)。図2において、図1と同等のものは同一符号を付してあり、買参人端末A11中の11bはクロック信号を発生するクロック信号発生部、11cは送受信部11aで前記競り開始信号を受信したときにはクロック信号発生部11bよりのクロック信号をカウントし、カウントデータを出力するカウンタである。買参人端末のその他のブロックについては図1と同機能のものである。

【0010】次に、本発明の動作につき図ごとに分けて説明する。

(1) 図1の実施例の説明

競りは、原則として各レーンそれぞれ別個に進行する。そこで、各レーンの競り人端末1、2、3は競りの開始に合わせ競り開始信号を各買参人端末側へ送信する。この場合、競り開始信号にはレーンを特定する信号を含める。この競り開始信号を基準信号として基準信号発生部1aより発生させるようにし、同発生部を各レーン毎に設ける。この競り開始信号は送受信部1b及びバスライン7を介し、各買参人端末4、5、6へ送信される。上記バスライン7は、例えば、前述(従来の技術)のCSMA/CD方式のLANで構成される。

【0011】各買参人端末4等には時計部4bを設けておき、同端末の送受信部4aにより上記競り開始信号を受信したときには同時計部4bより時報データを出力する。上記時報データは現在の時刻を意味するが、競りの性質を考慮し、秒単位以下の時刻を含めるようにしてもよい。この時報データを時報データ付加部4dにおいて入力部4cよりの応札データに付加する。尚、入力部4cにおいては、例えば図示のように、買参人を特定するための識別記号(ID)や購入数量等を入力する10キー41、入力した内容を表示する表示部42、応札の際に操作する応札キー43、上記IDや購入数量等を記憶するメモリ部44等が設けられている。上記の応札データはメモリ部44に記憶されたデータに応札キー43を操作したときのデータを含めたものを意味する。

【0012】この付加された信号が送受信部4a及びバスライン7を介し、参入しているレーンの競り人端末(ここでは、Aレーン競り人端末1とする)へ送信される。競り人端末1では送受信部1bで受信した各買参人端末よりの応札データそれぞれをメモリ部1cに記憶する。この場合、時間を区切り、その間に受信された応札データを

記憶する。判定部1dは制御部1fの制御の下、買参人端末側で応札データに付加された時報データの早い順を基準に買参人端末側発信順位を判定する。また、同判定結果を表示部1eに表示する。以上が図1の動作である。

【0013】(2) 図2の実施例の説明

図2は買参人端末側において、図1の時報データ付加に代え、カウントデータを付加するようにしたものである。競り人端末側は判別部1dによる判別対象が異なる点を除き図1の構成と同様である。このため、買参人端末側(買参人端末A11で説明)には図1の時計部に代え、クロック信号発生部11b、カウンタ11cを設けている。クロック信号発生部11bはクロック信号を発生している。ここで、送受信部11aが競り人端末よりの競り開始信号を受信すると、カウンタ11cが上記クロック信号のカウントを開始し、カウントデータを出力する。

【0014】このカウントデータをカウントデータ付加部11eで応札信号に付加する。このカウントデータを付加した応札信号は送受信部11a及びバスライン7を介し、競り人端末(例として、Aレーン競り人端末1)へ送信される。Aレーン競り人端末1側は図1の場合と同様、一旦、各買参人端末よりの応札データをメモリ部1cに記憶し、次いで、判定部1dにより各買参人端末の発信順位を判定する。この判定は、カウント値の少ない順を基準にする。この判定結果の表示については図1と同様である。以上が図2の動作である。

【0015】

【発明の効果】以上説明したように本発明によれば、C SMA/CD方式のLAN等により構築された競りシステムにおいて、競り人端末側から発した競り開始信号を基に買参人端末側で発生させた時報又はカウントデータのいずれかを買参人端末側の応札データに付加するようにしたので、競り人側では同時報またはカウントデータを基に正当な買参人端末側の発信順位を判定することができるようになる。これにより、従来、上記LAN接続*

*によるシステム構成において生じていた、バスライン上でのデータのぶつかり合いに起因した買参人側の発信順位と、競り人側の受信順位とが必ずしも一致しないという不具合があっても、正当な発信順位を判定することができることとなる。以上から、本発明により競りの公平性が維持される。

【図面の簡単な説明】

【図1】本発明による競りシステムの一実施例を示す要部ブロック図である。

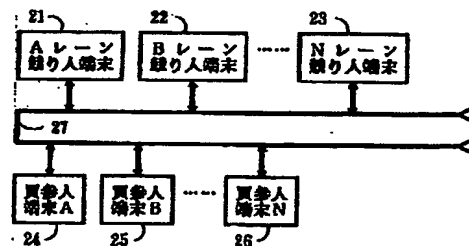
10 【図2】本発明による競りシステムの他の実施例を示す要部ブロック図である。

【図3】従来の競りシステムを説明するための競りシステム要部ブロック図である。

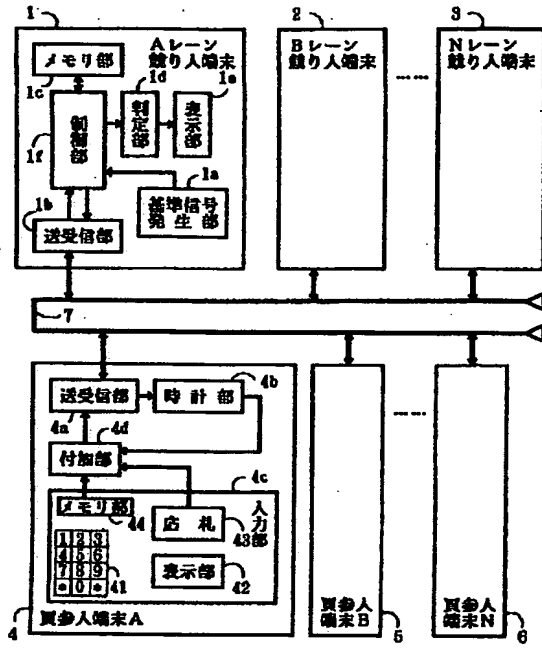
【符号の説明】

- 1 Aレーン競り人端末
- 1a 基準信号発生部
- 1b 送受信部
- 1c メモリ部
- 1d 判定部
- 1e 表示部
- 1f 制御部
- 2 Bレーン競り人端末
- 3 Nレーン競り人端末
- 4 買参人端末A
- 4a 送受信部
- 4b 時計部
- 4c 入力部
- 4d 時報データ付加部
- 5 買参人端末B
- 6 買参人端末N
- 7 バスライン
- 11b クロック信号発生部
- 11c カウンタ
- 11e カウントデータ付加部

【図3】



【図1】



【図2】

